## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Ole Bentz

 Serial No.
 : 09/651,159

 Filing Date
 : August 30, 2000

For : Overflow Detection and Clamping with Parallel Operand

2115

Processing for Fixed-Point Multipliers

Confirmation No. :
Group Unit :

 Group Unit
 :
 2193

 Examiner
 :
 Chat C. Do

 Customer No.
 :
 31870

CERTIFICATION UNDER 37 CFR 1.8(a) and 1.10

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Date: April 25, 2006

Commissioner for Patents

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## AMENDMENT AND RESPONSE UNDER 37 C.F.R. §1.111

Dear Sir:

This Amendment and Response replies to the Office Action mailed on January 25, 2006.

Introductory Comments begin on page 2 of this Response.

Amendments to the Detailed Description begin on page 3 of this Response.

Amendments to the Brief Description of the Drawings begin on page 4 of this

Response.

Amendments to the Claims begin on page 5 of this Response.

Remarks/Arguments begin on page 14 of this Response.

Replacement Drawing Sheet is in:

Attachment A

New Drawing Sheet is in:

Attachment B

## INTRODUCTORY COMMENTS

After careful review, Applicant hereby responds to a January 25, 2006 non-final Office Action regarding the above-referenced patent application. In view of this Response, Applicant respectfully requests reconsideration of this application.

Applicant has amended Claims 3, 6, 13, 17 and 18, amended Figure 2, and has added new FIGURE 6. New claim 21 has been added. Applicant has not added new matter with this Response. Indeed, Applicant only offers this Response to clarify the invention for the Examiner and to assist the Examiner's understanding of the same.

The Applicant is appreciative of the Examiners furthered indication that Claims 4-5 and 10-12 are allowable.

## AMENDMENTS TO THE DETAILED DESCRIPTION

The detailed description has been amended to clarify the Application in conjunction with new Figure 6. Please amend the specification on page 11, second to last paragraph, as follows:

As before, the accurate clamp prediction must use the (io+1) bit of the product, i.e., the product must be computed at least to (io+1) integer bits. As shown in Figure 6, this This bit is logically inverted, by inverter 70, then ORed logically with the simple clamp predictor to yield the accurate clamp determination.